

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Havemann et al.

Examiner: TBD

Serial No: TBD

Art Unit: TBD

Filed: 11/01/01

Docket No.: TI-27506

For: INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC AND
METHOD OF MANUFACTURING THE SAME

PRELIMINARY AMENDMENT

November 1, 2001

Assistant Commissioner for Patents

Washington, DC 20231

Dear Sir:

Please amend the above referenced application as follows:

In the Specification:

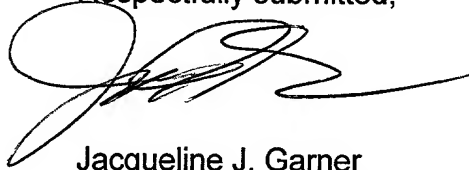
Page 1, before line 1, insert --This application claims priority under 35 USC §
119(e)(1) of provisional application numbers **60/251,494** filed **12/05/00**.--

REMARKS

Entry of the foregoing amendment prior to examination is respectfully requested.

If the Examiner has any questions or other correspondence regarding this application, Applicant requests that the Examiner contact Applicant's attorney at the below listed telephone number and address.

Respectfully submitted,



Jacqueline J. Garner
Attorney for Applicants
Reg. No. 36,144

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972) 271-1176
Fax: (972) 917-4417
or (972) 917-4418